

A Cross Layer Time Slot Reservation Protocol for Wireless Networks

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Abstract. The function of medium access control (MAC) protocol and the capacity of physical layer sufficiently affect the system performance in the wireless networks. In this research, we propose a MAC protocol based on direct sequence code division multiple access (DS-CDMA) for the wireless networks to guarantee quality-of-service (QoS) which depend on physical layer information, named the cross layer time slot reservation (CLTSR) protocol. A channel is divided into control and data channels to transmit control and data packets in the corresponding control and data frames, respectively. The data frame is further subdivided into several time slots; each time slot can transmit different traffic types such as constant bit rate (CBR), variable bit rate (VBR), and available bit rate (ABR). Each station maintains the available spreading code (ASC) table to understand which time slots and spreading codes have been reserved. CLTSR will allocate time slots and the spreading code for the data frame by using the fixed time slot allocation (FTSA) or the mixed time slot allocation (MTSA). The QoS can be guaranteed by providing the reservation of time slots and the spreading code. A Markov model is applied to analyze the CLTSR DS-CDMA system; the analytical and simulation results show that the proposed CLTSR performs has been improved.

Keywords: medium access control (MAC), direct sequence code division multiple access (DS-CDMA), cross layer, quality-of-service (QoS).

1 Introduction

Wireless local area network (WLAN) has been widely used recently, which uses IEEE 802.11x protocol to coordinate all stations. Each station contends with each other by using the carrier sense multiple access with collision avoidance (CSMA/CA) to access the channel [1],[2]. CSMA/CA uses backoff algorithm to avoid collision and reduce the collision probability in the wireless environment. Several backoff algorithms have been proposed to improve system performance [3],[4], however, the performance is still limited by the physical capacity due to the use of single shared channel. Therefore, it is necessary to improve the cross layer functions between the physical layer and MAC layer.

For many years, some researches regarding the random access networking based on direct sequence code division multiple access (DS-CDMA) are studied [5]-[7], because multiple access interference (MAI) may be possibly suppressed in MAC

layer. Generally in the controlled access CDMA (CA-CDMA) system, there exist control and data channels separately [8]. In the control channel, each station uses the signals of request-to-send (RTS) and clear-to-send (CTS) to contend the channel; and it also detects the degree of MAI to suitably adjust the transmitting power. However, this method needs more computational times and can not guarantee quality of service (QoS) in wireless network. Some MAC layer designs, such as the modified multi-carrier CDMA based on IEEE 802.11a [9], CDMA based ad hoc network [10] and adaptive acquisition collision avoidance multiple access (AACAMA) [11], just give the way to get spreading codes and cannot limit the number of the used spreading codes to suppress MAI. They also can not guarantee QoS in wireless network. In the distributed channel allocation protocol (DCAP) [12], each frame is divided into several time slots; each node contends to get a pair of time slot-spreading code (TC) to transmit data. Similarly, it cannot limit the number of the used spreading codes and the allocation of time-slots, hence system performance will be degraded especially while the allocating algorithm doesn't provide the different maximum numbers of spreading codes for the different traffic types such as constant bit rate (CBR), variable bit rate (VBR) and available bit rate (ABR). Furthermore, both RTS and CTS in DCAP contain all TC pairs such that their packet sizes become longer, hence system performance will be degraded in heavy traffic.

In this paper, we use the DS-CDMA technology in physical layer and propose a cross layer time slot reservation (CLTSR) protocol in MAC layer for wireless system. CLTSR also divides the channel into control channel and data channel; each station uses the modified RTS (MRTS) and modified CTS (MCTS), data slot request (DSR) and short ACK (SACK) to reserve time slots and spreading code in the data frame by four way hand shaking (MRTS-MCTS-DSR-SACK). CLTSR supports the maximum numbers of the spreading codes for different traffic types to suppress MAI. All reserved time slots and spreading codes are put in DSR such that both MRTS and MCTS can keep shorter sizes. We provide two kinds of time slot allocation algorithm in MAC layer: fixed time slot allocation (FTSA) and mixed time slot allocation (MTSA) to allocate time slots and spreading codes depending on the required bandwidth for each traffic type, therefore the guaranteed QoS and the improved system performance can be achieved.

Next section will describe the proposed CLTSR based on DS-CDMA technology in wireless ad hoc network. Section III will do the theoretical analysis using the finite state Markov chain, and section IV will simulate the system operation and then show that the result. Finally, the conclusion is drawn in section V.

2 Description of The Proposed CLTSR

To design CLTSR, two frame structures for control channel and data channel are used and shown in Fig. 1. Data can be transmitted in the k^{th} data frame according to the competitive result in the $(k-1)^{\text{th}}$ control frame. A data frame is subdivided into n time slots as shown in Fig. 2, which can support different traffic types such as CBR, VBR and ABR. In the DS-CDMA system, the maximum number of channels can be simultaneously used to transmit data for CBR, VBR and ABR depending on the

different tolerable bit error rate (BER, P_e) [13], which is assumed that BPSK modulation scheme is used in the additive white Gaussian noise (AWGN) channel as shown in Eq. (1) [14],

$$P_e = Q(\sqrt{3N/K-1}) \quad (1)$$

where N is the spreading factor, and K is the maximum number of channels used to transmit simultaneously, and $Q(x)$ is the complementary error function. We assume the maximum sets of spreading codes to transmit data for CBR, VBR and ABR are 41, 17, 11 corresponding to the bit error rate of 10^{-3} , 10^{-6} , 10^{-9} , respectively, if the spreading factor is 128.

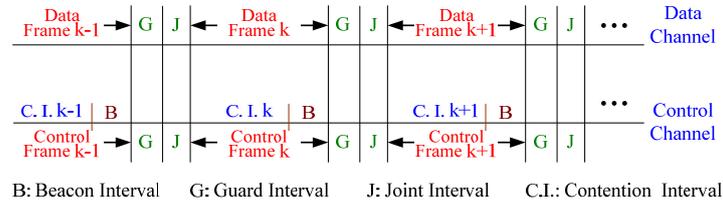


Fig.1. The structure of control frame and data frame

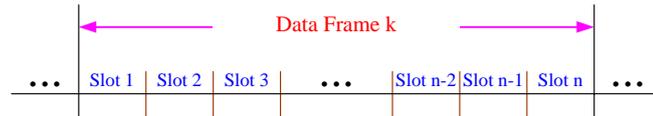
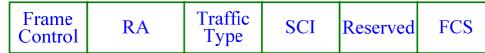


Fig. 2. The format of data frame

A control frame is divided into contention interval and beacon interval. The beacon interval is used to maintain system synchronization, while the contention interval is used to get the permission to send data in the next data frame. The contention is done by CSMA/CA with four-way hand shaking (MRTS-MCTS-DSR-SACK) procedure, where the formats of the corresponding control packets are shown in Fig. 3.



(a) The format of MRTS



(b) The format of MCTS



(c) The format of DSR



(d) The format of SACK

Fig. 3. The format of control packets

The field of traffic type may consist of two bits to represent three traffic types. The spreading code index (SCI) is indicated in the field of “SCI”, which may have six bits to indicate at most 41 sets of spreading codes. The fields of “RA” and “TA” represent the MAC addresses for receiver and transmitter respectively. The field of the slot number reservation (SNR) is used to reserve the number of time slots to be transmitted in the data frame, whose content depends on the transmission bandwidth requirement. Let N_s be the number of time slots to be reserved in the data frame and to be obtained by Eq. (2).

$$N_s = \left\lceil \frac{r_b}{r_f * b_s} \right\rceil \quad (2)$$

where r_b is the required transmission bandwidth in bit per second (bps); r_f is the number of data frames per second; and b_s is the transmitted bits in each time slot. The field of “Slot Number” represents the reserved time slots in the data frame, and the amount of “Slot Number” is the same as SNR. Thus, the size of DSR packet depends on how many time slots are needed. The functions of “Frame Control” and “FCS” are the same as the IEEE 802.11 standard [15].

Next, we introduce the procedure of CLTSR to access channel through the control packets.

- Step 1: Use allocation algorithm to reserve time slots and spreading codes for transmission in the next data frame.
- Step 2: Use the four-way handshaking procedure to contend and get the permission of time slots and spreading codes.
- Step 3: Monitor the DSR packets from other stations to check the confliction. It must run time slot allocation algorithm again when the content of its DSR packet conflicts with other stations, and go to step 2.
- Step 4: Use those time slots and spreading codes to transmit frames until the transmission finished, then releases the time slots and spreading codes.

Table 1. Initialization of ASC Table

		Slot 1	Slot 2	...	Slot n-1	Slot n
Traffic types		0	0	...	0	0
SCI	1	TCI(1,1)	TCI(1,2)	...	TCI(1,n-1)	TCI(1,n)
	2	TCI(2,1)	TCI(2,2)	...	TCI(2,n-1)	TCI(2,n)
		:	:	...	:	:
	11	TCI(11,1)	TCI(11,2)	...	TCI(11,n-1)	TCI(11,n)
		:	:	...	:	:
	17	TCI(17,1)	TCI(17,2)	...	TCI(17,n-1)	TCI(17,n)
		:	:	...	:	:
	41	TCI(41,1)	TCI(41,2)	...	TCI(41,n-1)	TCI(41,n)

Each station in the system must cooperate each other well by some distributed resource allocation algorithms because there has no base station. FTSA and MTSA allocate time slots and spreading codes according to the available spreading code

(ASC) table in each station, where the initialization of ASC table is shown in table 1. A time slot spreading code index (TCI) pair is a pair of time slot and spreading code in the ASC table, while the marked TCI pair in ASC table means that this TCI pair has been reserved already. The traffic type in each time slot is decided by the first station of using it, which is set to 0, 1, or 2 for CBR, VBR, and ABR, respectively. The FTSA algorithm is described as follows.

- Step 1: Search from ASC table to find all time slots having the same traffic type and available TCI pairs relative to those time slots.
- Step 2: Allocate number of TCI pairs which obtained by Eq. (2) from those available TCI pairs with a spreading code in best fit.
- Step 3: Try to find enough free time slots and set them to this traffic type if the TCI pairs is not enough.
- Step 4: Go to step 1 if there are still no enough free time slots, then the station enters blocking state.

The MTSA algorithm is obtained by modifying the FTSA algorithm and described as follows.

- Step 1: The same as step 1 in FTSA.
- Step 2: The same as step 2 in FTSA.
- Step 3: The same as step 3 in FTSA.
- Step 4: Try to find TCI pairs of VBR traffic to be used for CBR traffic.
- Step 5: The same as step 4 in FTSA.

3 Theoretical Analysis

This section analyzes the proposed CLTSR with FTSA because MTSA is similar to FTSA. We assume that the arrival traffics are the Poisson process with the mean arrival rates λ_C , λ_V , and λ_A , for the traffic types CBR, VBR and ABR, respectively. The service processes are also assumed to be exponentially distributed with the mean service rates μ_C , μ_V , and μ_A , for the traffic types CBR, VBR and ABR, respectively. Throughput can be obtained depending on how many TCI pairs are occupied by each traffic type. The TCI pair occupied by any traffic can be modeled by Markov chain. We assume that there are n time slots in each data frame, and the maximum number of TCI pairs can be occupied by CBR, VBR, and ABR are n_C , n_V , and n_A , respectively, but Eq.(3) must be satisfied.

$$\left\lceil \frac{n_C}{41} \right\rceil + \left\lceil \frac{n_V}{17} \right\rceil + \left\lceil \frac{n_A}{11} \right\rceil \leq n \quad (3)$$

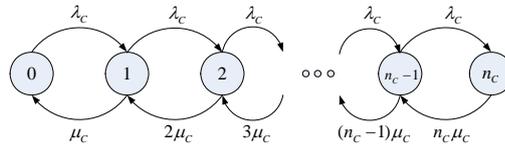


Fig. 5 State transition diagram of the CBR traffic

The number of TCI pair occupied by CBR traffic can be modeled by Markov chain with n_C states and its state transition diagram is shown as Fig. 5. We assume that only one TCI pair can be occupied whenever the CBR type traffic arrives. The state transition of the Markov chain can be characterized by a generator matrix. Let us denote \mathbf{Q}_C be the generator matrix for CBR traffic shown as Eq. (4), while \mathbf{P}_C represents the steady state probability vector for CBR traffic, where $\mathbf{P}_C = [p_{C0} \ p_{C1} \ p_{C2} \ \cdots \ p_{Cn_C}]$. The element of \mathbf{P}_C represents the steady state probability of each state in Fig. 5, which can be obtained by solving the balance equation shown as Eq. (5).

$$\mathbf{Q}_C = \begin{bmatrix} -\lambda_C & \lambda_C & 0 & 0 & 0 & 0 & 0 \\ \mu_C & -\lambda_C - \mu_C & \lambda_C & 0 & 0 & 0 & 0 \\ 0 & 2\mu_C & -\lambda_C - 2\mu_C & \lambda_C & 0 & 0 & 0 \\ 0 & 0 & 3\mu_C & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & \cdots & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & \lambda_C \\ 0 & 0 & 0 & 0 & (n_C - 1)\mu_C & -\lambda_C - (n_C - 1)\mu_C & \lambda_C \\ 0 & 0 & 0 & 0 & 0 & n_C\mu_C & -n_C\mu_C \end{bmatrix} \quad (4)$$

Then, we have $\mathbf{P}_C \mathbf{Q}_C = \mathbf{0}$ (5)

Similarly, the generator matrices for VBR and ABR traffics can also be obtained.

The TCI pairs occupied by VBR or ABR traffics are random variables, because the required bandwidths for VBR and ABR traffics are also random variables; therefore, we should transform the distribution of bandwidth requirement to the distribution of TCI pairs' requirement for VBR and ABR traffics. Let us denote $f_{VR}(r)$ be the probability density function of the bandwidth requirement for VBR traffic with boundary between R_{min} and R_{max} (bps). Let m_l and m_u represent the minimum and maximum number of TCI pairs occupied by VBR traffic and have the relationship with $m_u > m_l > 1$, where $m_l = \frac{R_{min}}{B_s}$ and $m_u = \frac{R_{max}}{B_s}$ if B_s is the transmission rate of a

TCI pair in data frames per second. The probability distribution of the number of TCI pairs' requirement for VBR traffic can be represented by a vector $\mathbf{b}_V = [b_{V0} \ b_{V1} \ b_{V2} \ \cdots \ b_{Vm}]$, where $m = m_u - m_l$. The element b_{Vk} ($k = 0, 1, \dots, m$) in vector \mathbf{b}_V represents the probability that there are $(m_l + k)$ TCI pairs required by VBR traffic and can be calculated by Eq. (5).

$$b_{Vk} = \begin{cases} \int_0^{R_{min}} f_{VR}(r) dr, & \text{for } k = 0 \\ \int_{(m_l+k-1)B_s}^{(m_l+k)B_s} f_{VR}(r) dr, & \text{for } k = 1, 2, \dots, m-1 \\ \int_{(m_u-1)B_s}^{\infty} f_{VR}(r) dr, & \text{for } k = m \end{cases} \quad (6)$$

Note that $\sum_{k=0}^m b_{Vk} = 1$. Actually, CBR traffic can be regarded as a special case of VBR traffic, as $m_l = m_u = 1$ and $m = 0$. Therefore, the arrival and departure rates for VBR traffic from a state can be obtained by the vectors $\lambda_V \mathbf{b}_V$ and $\mu_V \mathbf{b}_V$, respectively.

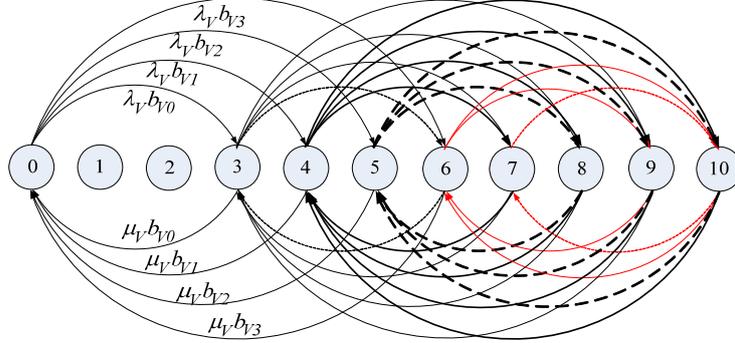


Fig. 6 Example state transition diagram of VBR traffic

For example, if m_l and m_u equal 3 and 6, respectively, then m equals 3. If n_V equals 10, the state transition diagram is shown as Fig. 6 and the reduced generator matrix \mathbf{Q}_V has the dimension of 9×9 shown as Eq. (7). In Fig. 6, it is obvious that the states 1 and 2 will never be reached so that their probabilities are zero. The transition probabilities $p_{01}, p_{02}, \dots, p_{0(m_l-1)}$ and $p_{10}, p_{20}, \dots, p_{(m_l-1)0}$ will be zero, because the minimum requirement of TCI pairs is $m_l > 1$. Some states may have more than one possible transitions for $m \geq m_l$. For example, the 6th state may be transitioned from the initial state with probability $\lambda_V b_{V3}$ or from the 3rd state with probability $\lambda_V b_{V0}$, while the transition rate from the 9th state to the 6th state can be obtained to be $3\mu_V b_{V0}^2 + 2\mu_V b_{V0} b_{V3}$.

$$\mathbf{Q}_V = \begin{bmatrix} q_{v00} & \lambda_V b_{V0} & \lambda_V b_{V1} & \lambda_V b_{V2} & \lambda_V b_{V3} & 0 & 0 & 0 & 0 \\ \mu_V b_{V0} & q_{v11} & 0 & 0 & \lambda_V b_{V0} & \lambda_V b_{V1} & \lambda_V b_{V2} & \lambda_V b_{V3} & 0 \\ \mu_V b_{V1} & 0 & q_{v22} & 0 & 0 & \lambda_V b_{V0} & \lambda_V b_{V1} & \lambda_V b_{V2} & \lambda_V b_{V3} \\ \mu_V b_{V2} & 0 & 0 & q_{v33} & 0 & 0 & \lambda_V b_{V0} & \lambda_V b_{V1} & \lambda_V b_{V2} \\ \mu_V b_{V3} & 2\mu_V b_{V0} & 0 & 0 & q_{v44} & 0 & 0 & \lambda_V b_{V0} & \lambda_V b_{V1} \\ 0 & 2\mu_V b_{V1} & 2\mu_V b_{V0} & 0 & 0 & q_{v55} & 0 & 0 & \lambda_V b_{V0} \\ 0 & 2\mu_V b_{V2} & 2\mu_V b_{V1} & 2\mu_V b_{V0} & 0 & 0 & q_{v66} & 0 & 0 \\ 0 & 2\mu_V b_{V3} & 2\mu_V b_{V2} & 2\mu_V b_{V1} & 3\mu_V b_{V0}^2 + 2\mu_V b_{V0} b_{V3} & 0 & 0 & q_{v77} & 0 \\ 0 & 0 & 2\mu_V b_{V3} & 2\mu_V b_{V2} & 3\mu_V b_{V1} b_{V0} + 2\mu_V b_{V1} b_{V3} & 3\mu_V b_{V0} & 0 & 0 & q_{v88} \end{bmatrix} \quad (7)$$

Similarly, the reduced generator matrix for ABR traffic \mathbf{Q}_A can also be obtained by substituting λ_A and μ_A for λ_V and μ_V , respectively. The steady state probability vector for ABR traffic \mathbf{p}_A can be obtained by solving the balance equation shown as Eq. (8).

$$\mathbf{p}_A \mathbf{Q}_A = \mathbf{0} \quad (8)$$

4 Simulation Results

In this section, we simulate the proposed CLTSR system by using Visual C++ program and compare with the theoretical analysis. Let us assume that data channel has the bandwidth of 10.6 Mbps; each time slot has the length 2 kbits (about

0.1887ms); each data frame has 164 time slots (about 30.94ms); and there yields about 32 data frames per second. Let us also assume that the arrival traffics of CBR, VBR and ABR occupy 19%, 4% and 77%, respectively. The average bandwidth requirement for CBR, VBR and ABR traffics are 64, 512, and 256 kbps, respectively. Similarly, the mean access duration for CBR, VBR and ABR traffics are 5, 10 and 1.56 minutes, respectively. Throughput is calculated by $(N_{pkt} \times l) / T_{sim}$, where l , N_{pkt} and T_{sim} are the packet length, the total number of the successful transmitted packets and the simulation time. The blocking probability is obtained by $N_b / (N_b + N_s)$ where N_b and N_s are the number of blocking and success packets, respectively.

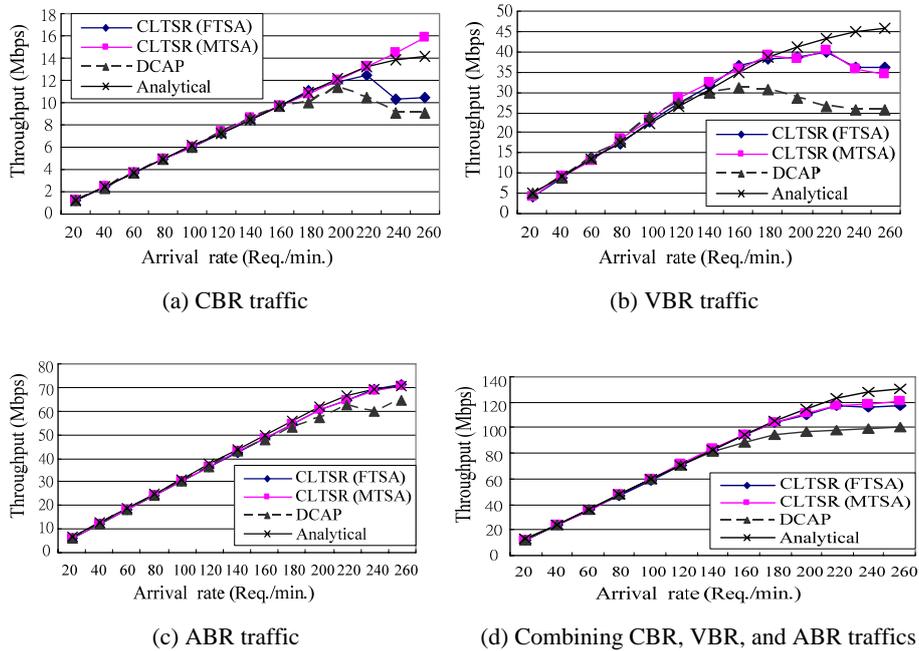


Fig. 7 Throughput versus arrival rate for CLTSR, DCAP and analytical solutions

Fig. 7 shows the simulated throughput for FTSA/MTSA in CLTSR, DCAP and theoretical analysis obtained from the Markov model above. We see that the proposed Markov model fits the simulation results very well except arrival traffic higher than 220 (Req./min.). Although the CBR traffic occupy 19% of arrival traffic, the allowable 41 sets of spreading codes in a time slot and 64Kbps constant bandwidth requirement will make it occupying time slot in the data frame slower than ABR traffic in almost no available time slots can be used. And it also means that when the arrival traffic is heavy (over 220 (Req./min.)), the throughput of CBR traffic will lower than theoretical analysis. But once the MTSA is used, we can see the throughput of CBR traffic even higher than theoretical analysis, as shown in Fig. 7 (b). One way to make throughput always near the theoretical analysis is expanded time slot or channel capacity in the data channel. Of course, due to the DCAP hasn't good time slot allocation algorithm to allocate time slot, it will have lower performance than CLTSR system. Similarly, the VBR traffic has same situation as the CBR traffic,

as shown in Fig. 7 (c). And we can see the throughput of VBR using MTSA is same as using FTSA. So, the MTSA never affect the throughput in VBR traffic but enhance the throughput in CBR traffic. In the Fig. 7 (d), the FTSA, MTSA and theoretical analysis are almost same everywhere in the ABR traffic. Fewer sets of spreading codes, smaller access duration and higher occupation of arrival traffic make ABR traffic has more stable situation.

5 Conclusions

We have proposed the CLTSR for DS-CDMA based wireless network. The special reservation protocol which combines the function of DS-CDMA in physical layer and time slots/spreading code allocation in MAC layer not only improve system performance but also provide the guarantee of QoS. It is shown that the Markov chain models are accurate in predicting the behavior of CLTSR under the assumption that the arrival traffic has Poisson distribution. The MTSA is suggested to use because the performance of VBR and ABR traffic are almost same in both FTSA and MTSA, while the MTSA has better performance in CBR traffic. This is a special MAC protocol design dependent on the capacity of physical layer. Due to the advance multiple access and channel estimation technology in wireless communication networking, we need a better MAC protocol to use them. Then, the performance with respect to the entire system can have the best situation. This is the motive that we begin to design the CLTSR.

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