A Disassembly Model based on Polychromatic Sets Theory for Manufacturing Systems

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Abstract: Existing disassembly models are inefficient in generation of disassembly sequence and the number of disassembly schemes tends to be explosive in combination. In addition, few models have focused on three-dimension disassembly. This paper defines the concept of connectivity of part unit, proposes the generation algorithms of connective part units, and then develops a new disassembly model based on polychromatic sets theory. Adopting possible displacement model in polychromatic sets theory as disassembling model, all connective part units are sieved by possible displacement function groups to eliminate illogical ones. Hierarchical graph is employed to describe disassembly process and disassembly sequence. Present disassembly model based on polychromatic sets is convenient for expressing restriction relation between each two parts in disassembly process. It also has advantages of higher formation level, avoiding "combination explosion" and tending to program.

1 Introduction

Disassembly is a very important and difficult process in green remanufacturing engineering. The premise of recycling and reusing of worn-out products is that the

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parts of products that have arrived at their lifecycle and need recycling can be easily taken down. So before disassembling of products, disassembly process should be programmed properly and be optimized with the aims such as economics of disassembling, coordinating of environment and feasibility of technique. Finally optimal disassembly sequences can be obtained [1, 2].

In the literature there are many papers proposing various methods for studying disassembly and recycling. Moore (1998) proposed an algorithm which automatically generates a disassembly Petri Net (DDN) [3]. From a geometrically based precedence matrix, Gungor (1998) addressed the uncertainty related difficulties in disassembly sequence planning [4]. Then Lambert (2002) studied disassembly sequence generation for electric equipments [5]. Kongar (2002) presented a genetic algorithm for disassembly process planning [6]. Park (2003) proposed that a methodology generates an optimal disassembly sequence by determining disassembly methods, employing PLM (profit-loss margin) curve to determine disassembly depth, and using MRFD (modified reverse fishbone diagram) to generate optimal disassembly sequence [7]. Gupta (2004) studied disassembly problem based on a case of cell phone [8]. Gonzalez (2004) presented a scatter search (SS), in which metaheuristic is presented aiming to deal with the optimum disassembly sequence problem for the case of complex products with sequencedependent disassembly costs [9]. Chung (2004) presented an integrated approach to selective-disassembly sequence planning [10]. Villalba (2004) employed a recyclability index of material to determine economic of disassembly [11].

However, although many approaches to generate disassembly sequence have been developed in above literature, they have some common disadvantages as follows. They can not sieve illogical disassembly sequence, the number of disassembly schemes tends to be explosive in combination, it is difficult for them to differentiate different relations and to generate disassembly sequence, and few researches have focus on three-dimension disassembly [12].

2 Connectivity of Part Units

Connective part units are the really existent units in disassembly process which are composed of less than two parts. Their importance lies in their real existence in disassembly process. Connectivity of part units is based on the adjacent relation of parts which means that if two parts of the product have interface, they are adjacent [13]. To get generation algorithms of connective part units, firstly several definitions are made as follows.

Defination 1: If there are n parts in a product, then k-rank sub-graph of adjacent graph of the product can be formalized as Boolean vector $\boldsymbol{X}^k = (X_i)$, where if part i belongs to the sub-graph, then $X_i = 1$. Or $X_i = 0$. Call X^k k-rank Boolean vector of adjacent graph and it is denoted as $\|\boldsymbol{X}^k\| = k$. All k-rank Boolean vectors of

adjacent graph compose k-rank Boolean vector set which is noted as $X^{(k)}$. $X^{(k)} = \left\{ X^k \mid \mid X^k \mid = k \right\}.$ **Defination 2:** With X₁ and X₂ as base, defining binary relation as follows:

 $R = \left\{\left\langle x_1, x_2 \right\rangle \middle\| \middle\| x_1 \oplus x_2 \middle\|_1 = k, x_1 \in X_1, x_2 \in X_2 \right\}, \quad \text{so there exist that } \mathbf{Y}$ ($Y \in Y^k$), $Y = \left\{ x_1 \oplus x_2 \left| \left\langle x_1, x_2 \right\rangle \in R \right\} \right.$ and call Y k-rank combinatorial Boolean vector of X_1 and X_2 . Y is denoted as $Y = (x_1, x_2)_{\oplus}^k$.

Reasoning 1: provided that It has been known that there are n parts in some product, its adjacent matrix is $A_p = (a_{ii})$. If $a_{ii} = 1 (1 \le i < j \le n)$, then $S^2 = (S_i)$ can be constructed, Where there is only $S_i = S_j = 1$. $S^{(2)}$ may be constructed using S^2 as elements.

Reasoning 2: If $S^{(k-1)}$ is known, then $S^{(k)} = \left(S^{(k-1)}, S^{(k-1)}\right)\Big|_{\oplus}^k$. On the foundation of above definitions and reasoning, the generation algorithm

of connective part units is listed as follows:

Generation flow of connective part units is showed in Figure 1.

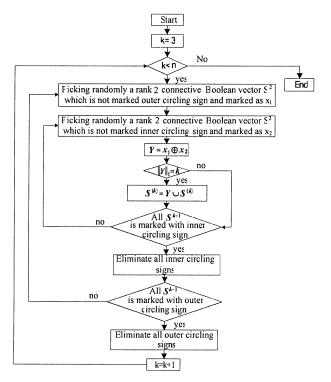


Fig. 1. Generation flow chart of connective part units

Step1: Let k=3.

Step2: To pick randomly a 2-rank connective Boolean vector S^2 which is not marked outer circling sign, and mark it as x_1 .

Step3: To pick randomly a 2-rank connective Boolean vector S^2 which is not marked inner circling sign, and mark it as x_2 .

Step4: To calculate Boolean sum of x_1 and x_2 , that is $x_1 \oplus x_2$. And let it equal Y. **Step5:** To determine whether Y is 3-rank Boolean vector or not. If yes, then $S^k = S^k \cup Y$. Or turn to step6.

Step6: To determine whether there are still 2-rank Boolean vectors which are not marked with inner circling signs. If yes, turn to step3. Or cancel all 2-rank Boolean inner signs and turn to step7.

Step7: To determine whether there are still 2-rank Boolean vectors which are not marked with outer circling signs. If yes, turn to step2. Or to eliminate all outer circling signs of 2-rank Boolean vectors, then turn to step8.

Step8: To determine whether k+1 is smaller than n. If yes, let k=k+1. Or turn to end.

3 Disassembling Model

For a connective part unit, the process of determining whether it can generate part units on next lower level by taking down one part from it is called disassembling determining. This paper advances a kind of possible displacement model of part and uses possible displacement equation groups as disassembling sieve to eliminate illogical part units from connective part units set.

3.1 Disassembling model

Generally, the premise of disassembly of a part is that there should be no any blockage on its disassembly channel. That is to be sure that disassembly channel exits. Possible displacement model describes blockage relation in disassembly in the direction $\pm X$, $\pm Y$, $\pm Z$. Logical equation which is constituted with a group of parts that block the disassembly channel of some part is called possible displacement equation of the part [14]. Possible displacement equation expresses channel blockage relation in disassembly. For example, for part a_k , if its disassembly channel is blocked, then $W(a_k)=1$. On the contrary, $W(a_k)=0$. For the part a_j which is a element constituting possible displacement equation of a_k , if it is disassembled before a_k , then its logical value in equation $W(a_k)$ is $a_j=0$. On the contrary, $a_j=1$. Logic relation between each two parts in possible displacement equation is AND/OR. Possible displacement equations of all parts constitute possible displacement model of product.

Table 1 below shows possible displacement model where (a_i, a_k) denotes logical relation of a_k to a_j , F_1 - F_6 denotes blockage relation in the direction $\pm X$, $\pm Y$, $\pm Z$ and • denotes blockage of channel.

	F1	F2	F3	F4	F5	F6
(a_1,a_2)	•					•
•••						
(a_3,a_4)		•				•
•••			•			
(a_5,a_7)				•		•
•••	•					
•••						
(a_{n-1},a_n)	•					•

Table 1. Possible displacement model

3.2 Obtaining algorithms of possible displacement equation groups

To obtain possible displacement equation groups from possible displacement model, below definitions are given:

- $(1)F_1$ - F_6 shows blockage relation of movement in the direction $\pm X$, $\pm Y$, $\pm Z$.
- (2) $F_i(a_k, a_j)$ show the blockage relation of a_j to a_k where $1 \le i \le 6, 1 \le k, j \le n$, and $j \ne k$ (n is the total number of parts).
- (3) Possible displacement relation of a_j to a_k is different from a_k to a_j . That is $F_i(a_k,a_j) \neq F_i(a_j,a_k)$. Obviously, $F_1(a_k,a_j)=F_2(a_j,a_k)$, $F_3(a_k,a_j)=F_4(a_j,a_k)$ and $F_5(a_k,a_j)=F_6(a_j,a_k)$.

Algorithm of obtaining possible displacement groups is as follows:

- (1) To extract possible displacement information from possible displacement model in table 1.
 - (2) Obtaining steps:

Step1: To determine whether part a_k is interfered.

$$W(a_k) = \bigvee_{j=1}^{n} \left[\bigvee_{j=1}^{6} F_i(a_k, a_j) \right]$$
, if $W(a_k) = 0$, then it means that part a_k is not

interfered. Or if $W(a_k)=1$, then part a_k is interfered.

Step2: To obtain combination of parts that interfere a_k .

If
$$\bigvee_{i=1}^{6} [F_i(a_k, a_j) \vee F_i(a_k, a_m)] = 1$$
, then $W(a_k) = a_j \wedge a_m$.

Step3: Combinatorial law of polychromatic sets: if $W(a_k) = a_j \wedge a_m$, $W(a_k) = a_j \wedge a_m$. Then $W(a_k) = a_j \wedge (a_m \vee a_l)$.

3.3 Generation algorithm of disassembly part unit

Determining disassembling of all connective part units by using possible displacement equation groups, those part units that are determined as disassembling ones are called sub-part units, generation algorithm of which is as follows:

Figure 2 shows generation flow of part unit.

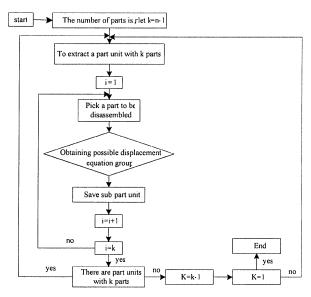


Fig. 2. Flow chart of generation of disassembly unit

Step1: To extract all part combinations with n-1 parts and pick out those combinations that meet possible displacement groups, they are sub-part units with n-1 parts and save them to unit database.

Step2: To pick a part unit composed of k parts.

Step3: To pick a part from k parts of the part unit of step2 as the disassembled one.

Step4: To determine whether every part to be disassembled meets its possible displacement equation groups. If yes, then the disassembly is proper and save the part unit with k-1 parts to sub-part unit database.

Step5: To determine whether every part has been selected as disassembled one. If yes, then turn to step6. Or turn to step3.

Step6: To determine whether there are part units with k parts. If yes, then turn to step2. Or let k=k-1.

Step7: To determine whether k is smaller than 1. If yes, then stop. Or turn to step2.

4 Generation Method of Disassembly Sequence

On the basis of obtaining possible displacement model, generation method of disassembly sequence is detailed as follows. Computer reads in possible displacement model and saves it in matrix, then searches every line of the matrix and calculates cumulative constrains of each part. Disassembly sequence can be

generated according to the principle that part which has less cumulative constrains be disassembled firstly.

5 Disassembly Hierarchical Graph

After obtaining sub-part unit, we employ hierarchical graph of polychromatic sets to express level relation in disassembly [15], where set of vertexes is:

$$A = (A_1, A_1^{\parallel}, A_2^{\parallel}, \dots, A_k^{\parallel}, A_l^{\parallel}, \dots, A_m^{N}, a_1, a_2, \dots a_n).$$

Border $e_{i,j} \in$ connects node A_i^J and node A_j^{J+1} on the next lower level. Borders in graph G = (A, E) are composed as follows:

$$E \ni e_{i(j)} = \begin{cases} 1, \text{ if } A_i^J \ni A_j^{J+1} \\ 0, \text{ on the contrary} \end{cases}$$

Steps of building hierarchical graph are as follows:

Step1: Root node of the hierarchical graph is the set of all parts of product.

Step2: Beginning from root node, nodes on every level can be decompounded into one disassembly unit on next lower level and a single part. The disassembly unit can be decompounded once again. For example, node of disassembly unit in rank J can be decompounded into the elements of rank (J+1) including the disassembly unit of A_{J+1} and the part of a_k . A_{J+1} and a_k meet the condition of $A_{J+1} \cup a_k = A_J$, k < n, and A_{J+1} can be divided once again.

Step3: Similarly, the hierarchical graph has been built when every disassembly unit has been decompounded into one single part, and then the disassembly sequence of product can be obtained from the hierarchy graph expediently.

6 Case Analyzing

Take PC box as an example to show application of present disassembly model in three-dimension disassembly. In Figure 3 each number denotes separately as follows. 1-box, 2-graphical card, 3-audio card, 4-motherboard, 5-speaker, 6-harddisk, 7-soft driver, 8-shelf, 9-power.

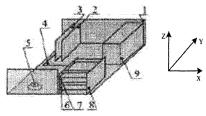


Fig. 3. Sketch map of PC box

(1) Connective part units

Total number of part units with n parts is $(2^n - n - 2)$. For this example, n is 9, so total number of part units is 501. Number of connective part units is calculated as 164.

(2) Possible displacement model

Possible displacement model of PC box is showed in Table 2. And possible displacement equation groups are as follows:

$$W(a_1) = a_4 \lor a_5 \lor a_8$$
, $W(a_2) = 0$, $W(a_3) = 0$ $W(a_4) = a_2 \lor a_3 \lor a_7$, $W(a_5) = 0$, $W(a_6) = a_7$, $W(a_7) = 0$, $W(a_8) = a_6 \lor a_7$, $W(a_9) = 0$.

Table 2. Possible displacement model of PC box

	F1	F2	F3	F4	F5	F6
(a_1, a_2)				•	•	
(a_1,a_3)				•	•	
(a_1,a_4)				•	•	
$(\mathbf{a}_1,\mathbf{a}_5)$					•	•
$(\mathbf{a}_1,\mathbf{a}_6)$					•	
(a_1,a_7)					•	
(a_1,a_8)			•		•	
$(\mathbf{a}_1,\mathbf{a}_9)$				•	•	
(a_2, a_4)	•	•	•	•		•
(a_3, a_4)	•	•	•	•		•
(a_4, a_9)					•	
(a_6, a_7)					•	
(a_1,a_2)	•	•	•		•	•
(a_7, a_8)	•	•	•		•	•

(3) Obtaining of sub disassembly unit

By using generation algorithms of disassembly unit, number of part units on every level is calculated as following:

$$|A^{(8)}| = 5$$
, $|A^{(7)}| = 11$, $|A^{(6)}| = 14$, $|A^{(5)}| = 13$, $|A^{(4)}| = 13$, $|A^{(3)}| = 7$, $|A^{(2)}| = 3$.

(4) Disassembly sequence

Combining possible displacement model above and method in section 3, constrain number of every part is calculated as:

$$R(a_1) = 13$$
, $R(a_2) = 6$, $R(a_3) = 6$, $R(a_4) = 8$, $R(a_5) = 2$, $R(a_6) = 7$, $R(a_7) = 7$, $R(a_8) = 12$, $R(a_9) = 3$.

According to order of constrain number above, we can get the disassembly sequences as following:

$$(a_5 \rightarrow a_9 \rightarrow a_2 \rightarrow a_3 \rightarrow a_6 \rightarrow a_7 \rightarrow a_4 \rightarrow a_8 \rightarrow a_1)$$

$$(a_5 \rightarrow a_9 \rightarrow a_3 \rightarrow a_2 \rightarrow a_6 \rightarrow a_7 \rightarrow a_4 \rightarrow a_8 \rightarrow a_1)$$

$$(a_5 \rightarrow a_9 \rightarrow a_2 \rightarrow a_3 \rightarrow a_7 \rightarrow a_6 \rightarrow a_4 \rightarrow a_8 \rightarrow a_1)$$

$$(a_5 \rightarrow a_9 \rightarrow a_3 \rightarrow a_2 \rightarrow a_7 \rightarrow a_6 \rightarrow a_4 \rightarrow a_8 \rightarrow a_1)$$

(5) Hierarchical graph

Hierarchical graph is built on the foundation of four disassembly sequences above. They are illustrated as follows (a), (b), (c) and (d) in Figure 4.

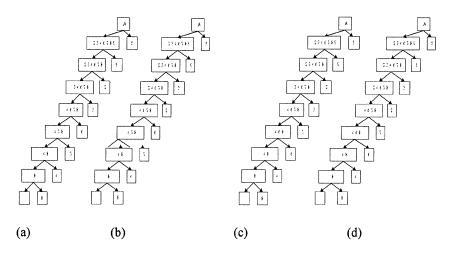


Fig. 4. Hierarchical graph

7 Conclusions

This paper presents concept of connectivity of part units and their generation algorithms. Connective part units are generated directly by using the generation algorithms of connective part units in order to avoid disturbing of many disconnection part units. Connective part units are sieved by possible displacement equation groups which are based on possible displacement model to eliminate illogical part units. It is effective to defend combinatorial explosion of disassembly schemes. Based on case, disassembly sequences are programmed according to the principle that part which has less cumulative constrains should be disassembled firstly, and the disassembly sequences which are consistent with real process of disassembly are obtained. Finally the process of disassembly is expressed by building hierarchical graph of polychromatic sets. This expression is concise and intuitionistic. Polychromatic sets theory adopts logical Boolean operation. It has following advantages: complexity of algorithms is less, easy to program and facilitate to extend to complex system.

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